



OFGS File No.: P/4076-19

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of

Ching Man STANLEY et al.

Date: May 26, 2005

Patent No.: 6,806,725

Issue Date: October 19, 2004

For: METHOD AND APPARATUS FOR PROCESSING AN ARRAY OF
PACKAGED SEMICONDUCTOR DEVICES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
JUN 06 2005

REQUEST FOR CERTIFICATE OF CORRECTION

of Correction

Sir:

Pursuant to 37 C.F.R. § 1.322(a), it is requested that the above-identified patent be corrected as follows:

In the Claims:

Claim 1, line 45, change "singulated" to --unsingulated--.

As this error appears to be the fault of the Patent Office, no fee is enclosed. Should any fee be required to issue the Certificate of Correction, the same may be charged to our Deposit Account No. 15-0700.

A Certificate of Correction (Form PTO/SB/44) is also enclosed.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on May 26, 2005:

Robert C. Faber
Name of applicant, assignee or
Registered Representative

Signature

May 26, 2005
Date of Signature

Respectfully submitted,

Robert C. Faber

Registration No.: 24,322

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RCF:mjb

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,806,725 *fj 2*

DATED : October 19, 2004

INVENTOR(S) : Ching Man Stanley Tsui, M. Bilan Curito, Jr., and Lap Kei Eric Chow

It is certified that error appears in the above identified patent and that said Letters Patent is hereby corrected as shown below.

In the Claims:

In claim 1, line 45, change "singulated" to --unsingulated--.

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PATENT NO. 6,806,725

⇒ 0

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,806,725 *B2*

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PATENT NO. 6,806,725

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If any additional payment is required, a check which includes the calculated fee of \$_____ (OFGS Check No. _____) is attached.

In the event the actual fee is greater than the payment submitted or is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

CONTINGENT EXTENSION REQUEST

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. §1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700.

AMENDMENTS

☐ If checked, amendment(s) to the specification are submitted herewith.

☐ If checked, an amended abstract is submitted herewith.

☒ If checked, amendment(s) to the claims are submitted herewith.

☐ If checked, amendment(s) to the drawings are submitted herewith.

Please amend the claims as indicated below:

1. (Currently amended) A method of processing an unsingulated array of packaged semiconductor devices ~~electronic component~~ comprising the steps of:
~~providing mounting means;~~ ^{of semiconductor devices}
mounting ~~the unsingulated electronic components onto the array on a~~ mounting means device; then
singulating the ~~components~~ packaged semiconductor devices to physically separate ~~them~~ the
packages; and then
testing the singulated ~~electronic components~~ packaged semiconductor devices for defects while they
are mounted on the mounting ~~means~~ device and without removal therefrom.
2. (Currently amended) A method according to claim 1, further comprising the step of
applying markings to distinguish non-defective ones of the ~~electronic components~~ packaged semiconductor devices from defective ones after testing while they are still mounted on the mounting
~~means~~ device.
3. (Previously presented) A method according to claim 2, wherein the singulation, testing
and marking steps are carried out at two or more stations.
4. (Currently amended) A method according to claim 3, including the step of moving the
~~electronic components~~ packaged semiconductor devices at least between the testing and marking
positions for testing and marking respectively.
5. (Currently amended) A method according to claim 1, ~~which includes~~ further including the
step of detecting the alignments of ~~electronic components~~ the packaged semiconductor devices
before testing, and orienting the array of ~~electronic components~~ packaged devices as desired before
~~implementing~~ testing.

opposite in that the individual dies are displaced from the plane of the other dice by stretching the diaphragm during testing (see col. 6, lines 26-42).

Claims 24 and 26 recite that "the packaged semiconductor devices are tested in subsets selected such that adjacent devices are not tested simultaneously." Gotman only tests one die at a time, and to the extent that references such as Atkins teach testing of multiple circuits simultaneously, there is no suggestion of testing in subsets selected such that adjacent devices are not tested simultaneously.

Claims 23-26 are accordingly allowable along with the other claims.

In view of the foregoing, favorable reconsideration and allowance of this application are respectfully solicited.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being transmitted via facsimile to (703) 872-9306 addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on February 25, 2004:

Lawrence A Hoffman

Name of Person Mailing Correspondence

Lawrence A Hoffman
Signature

February 25, 2004

Date of Signature

LAH:sks

Respectfully submitted,

Lawrence A Hoffman

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